Slovenská technická univerzita v Bratislave Fakulta informatiky a informačných technológií

Ilkovičova 2, 842 16, Bratislava 4

Tímový projekt



Export úloh z nástroja JIRA

Vedúci projektu: doc. Ing. Tibor Krajčovič, PhD.

Spolupráca: Ing. Lukáš Ondriga, Kistler Bratislava, s.r.o.

Názov tímu: TEST.IOT

Členovia tímu: Bc. Tomáš Bujna

Bc. Marián Ján Franko Bc. Rastislav Kováč

Bc. Igor Labát

Bc. Miroslav Sabo

Bc. Filip Starý

Bc. Stanislav Širka

Kontakt: fiit.tp.tim15@gmail.com

Akademický rok: 2018/2019

Summary	Issue key	Issue Type	Status	Assignee	Description	Epic Link	Epic Name	Sprint 1	Sprint 2	Sprint 3	Sprint 4	Sprint 5	Sprint 6	Sprint 7	Story Points	Task type
Summary	issue key	issue Type	Status	Assignee	The goal of the porject is to enable automatic testing of measuring devices.	Epic Link	Еріс капіе	Sprint 1	3print 2	эрппсэ	Spriit 4	эринсэ	Sprint	Spriit /	Story Politics	Task type
Project goal	IOT-78	Group	Group		For this purpose it is necessary to develop a device able to generate various analog and digital signals which will simulate sensors and device states.											
Configuration of	IOT-73	C	C		REST API should not be ComoNeo specific. It should be possible to use the											
various devices Robot Framework		Group	Group		same data model for other devices. Examples of robot framework tests demonstrates the functionality of											
Tests Configuration of	IOT-70	Group	Group		loTester.											
analog signals	IOT-72	Group	Group		It is possible to configure analog signals over REST API. First prototype of the device is used to test ComoNeo:Â											
Environment	IOT-71	Group	Group		[https://www.kistler.com/en/applications/industrial-process-											
Livioninent	101-71	Огоар	Отопр		control/plastic-process-monitoring/injection-molding-process-											
REST API	IOT-75	Group	Group		control/process-monitoring-with-comoneo/] On the basis of ComoNeo analysis create a REST API interface.											
Configuration of digital signals	IOT-77	Group	Group		It is possible to set digital input signals over REST API.											
Design Document how to	IOT-74	Group	Group													
use IoTester for devices other than	IOT-76	Group	Group													
ComoNeo High level	IOT CE	C			Architecture document contains high level view on PRU, ARM, beaglebone,											
architecture IoTester	IOT-65	Group	Group		robot framework and ComoNeo relations.Â											
architecture Architecture	IOT-69	Group	Group		Architecture of the IoTester software is documented.											
document Robot Framework	IOT-68	Group	Group													
tests tests	IOT-67	Group	Group													
Implementation	IOT-62	Group	Group		The goal of the implementation is to provide several working automated tests of the ComoNeo device.											
Hardware	IOT-63	Group	Group		Harware consists of reusable part and device specific part (e.g. ComoNeo connectors).											
Housing Tests integration	IOT-61	Group	Group		3D printer housing modelsĀ is designed.											
Tests integration into continuous	IOT-60	Group	Group													
Document how to																
use loTester for ComoNeo	IOT-64	Group	Group													
Robot framework	IOT-66	Group	Group													
Integration	IOT 55				REST API is documented. Documentation contains description how to use											
Documentation	IOT-56	Group	Group		the interface for different devices (not Lukáš Ondrigay for ComoNeo).											
Software Project goal	IOT-59	Group Group	Group													
Test examples implementation	IOT-55	Group	Group													
loTester implementation	IOT-58	Group	Group													
Design Interface between CPU and	IOT-105	Sub-task	To Do								IOT Sprint 4	IOT Sprint	IOT Sprint	IOT Sprint		
PRU					As I user I want to generate analog output on IoTester and test the							5	6	7		
					behaviour of ComoNeo firmware.											
C					Acceptance criteria:											
Create a test for ComoNeo analog	IOT-82	Story	To Do	Marián Ján Franko	* Test sets the measurement start of the ComoNeo to a pin connected to loTester	IOT-36								IOT Sprint 7		
input					* Test sets the analog output values to the IoTester (e.g. in 10 secondsÅ sets 10 different values)											
					* Test starts the measurement with digital output of IoTester * Test checks the values using cursor in ComoNeo web application (see the											
Test SPI without					attachment)								IOT Sprint	IOT Sprint		
PRU	IOT-120	Sub-task	To Do										6	7		
Measure if we are					l .											
able to write the	IOT-119	Sub-task	To Do										IOT Sprint	IOT Sprint		
able to write the whole curve to PRU	IOT-119	Sub-task	To Do		A Advant CD and January								IOT Sprint 6	IOT Sprint 7		
	IOT-119	Sub-task Sub-task	To Do		* Actual SD card image * Code composer											
whole curve to PRU					* Code composer * Connect to BBB								6 IOT Sprint	7 IOT Sprint		
whole curve to PRU					* Code composer * Connect to BBB Time: 8h								6 IOT Sprint	7 IOT Sprint		
whole curve to PRU	IOT-118	Sub-task	To Do	Bactielau KaudX	Code composer Connect to 888 Time: 8h Setup Device Tree								6 IOT Sprint	7 IOT Sprint		
whole curve to PRU				Rastislav Kováč	* Code composer **Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03								6 IOT Sprint 6	7 IOT Sprint 7		
whole curve to PRU	IOT-118	Sub-task	To Do	Rastislav Kováč	* Code composer **Comnect to BB8 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files.ps//TCZR1HLDT-FGK3ZH075/pins.png]								6 IOT Sprint 6	7 IOT Sprint 7		
whole curve to PRU	IOT-118	Sub-task	To Do		*Code composer *Commerc to BB8 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://files.slack.com/files.psr/TCZR1HLDT-FGK3ZH075/pins.png] Å								6 IOT Sprint 6	7 IOT Sprint 7		
whole curve to PRU	IOT-118	Sub-task	To Do		* Code composer **Comnect to BB8 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files.ps//TCZR1HLDT-FGK3ZH075/pins.png]								6 IOT Sprint 6	7 IOT Sprint 7		
whole curve to PRU	IOT-118	Sub-task Sub-task	To Do		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGX3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria:								6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU	IOT-118	Sub-task Sub-task	To Do		*Code composer *Commerc to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,)	IOT-36					IOT Sprint 4	IOT Sprint	6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7	13.0	
whole curve to PRU BBB for All Setup Device Tree	IOT-118	Sub-task Sub-task	To Do		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lotester	ЮТ-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7	13.0	
whole curve to PRU BBB for All Setup Device Tree	IOT-118	Sub-task Sub-task	To Do		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGX2F075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lotlester *documentation of the message - will contain reasoningÅ *desumentation of the message - will contain reasoningÅ	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7	13.0	
whole curve to PRU BBB for All Setup Device Tree	IOT-118	Sub-task Sub-task	To Do		* Code composer **Comnect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://liles.slack.com/files-ps/TICZR1HLDT+GK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) ***message should be easy to use for PRU (no parsing, no caching in PRU,) ***message will support all digital outputs and analog outputs usable on loTester **occurrent and control of the message.** will contain reasoning.**	ЮТ-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7	13.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory	IOT-118	Sub-task Sub-task	To Do		*Code composer *Comnect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://llees.slack.com/files-ps/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance crieria: **message should be easy to use for PRU (no parsing, no caching in PRU,) ***message should be easy to use for PRU (no parsing, no caching in PRU,) ***message will support all digital outputs and analog outputs usable on loTester **documentation of the message- will contain reasoning. **The basic idea how to create this message in CPU is described **The desiration of the message- will contain reasoning. **The basic idea how to create this message in CPU is described **The desiration of the message- will contain reasoning. **The desiration of the message- will contain reason	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7	13.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design	IOT-118	Sub-task Sub-task Story	To Do Closed		*Code composer *Comnect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://liles.slack.com/files.psi/TCZR1HLDT+GK3ZH075/pins.png Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU, and CPU. *message will support all digital outputs and analog outputs usable on to leater *decumentation of the message. will contain reasoning & *the basic idea how to create this message in CPU is described As a developer of foTester I need to measure the size limit of PRU message system Acceptance criteria:						IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory	IOT-118	Sub-task Sub-task Story	To Do Closed		*Code composer *Comnect to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://lles.slack.com/files-ps/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **decumentation of the message - will contain reasoning & **the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system Acceptance criteria: **message representation of the message size limit **measure FWU message size						IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory	IOT-118	Sub-task Sub-task Story	To Do Closed		*Code composer *Comect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pr/TiCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of folTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lotTester *documentation of the message - will contain reasoning Å *the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message yestem Acceptance criteria: *message PRU message size limit *messure PRU message size limit *messure PRU message size limit						IOT Sprint 4:		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory	IOT-118	Sub-task Sub-task Story	To Do Closed		*Code composer *Comnect to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://lles.slack.com/files-ps/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **decumentation of the message - will contain reasoning & **the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system Acceptance criteria: **message representation of the message size limit **measure FWU message size						IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory	IOT-118	Sub-task Sub-task Story	To Do Closed		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pin: 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGX3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lottleter *documentation of the message - will contain reasoning& the basic lies to love to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message yetem Acceptance criteria: *measure PRU message size limit *measure I we are able to write the whole curve to PRU *Ac a developer of loTester I need to send a simple message to SPI interface *Acceptance criteria: *measure if we are able to write the whole curve to PRU *Acceptance criteria: *prepare a simple program to work with SPI interface - the program is						IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7		
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Comect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://files.slack.com/files-pri/TCZR1HLDT-FGX3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU, message will support all digital outputs and analog outputs usable on lotlester *documentation of the message - will contain reasoning* with the basic lobe also how to create this message in CPU is described As a developer of for Tester I need to measure the size limit of PRU message vietnem Acceptance criteria: *measure PRU message size limit *measure five are able to write the whole curve to PRU *Ac developer of forSeter I need to send a simple message to SPI interface *Acceptance criteria: *prepare a simple program to work with SPI interface - the program is compliable and possible to load into PRU.	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pin: 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGI3ZH075/pins.png] Å As a developer of forExter I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lottester *documentation of the message - will contain reasoning & the basic lies also how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message yetem Acceptance criteria: *messure PRU message size limit *messare if we are able to write the whole curve to PRU *As a developer of forExter I need to send a simple message to SPI interface *Acceptance criteria: *prepare a simple program to work with SPI interface - the program is compliable and possible to load into PRU *anabled SPI and GPOs which are necessary to control DAC in the device tree *send simply message to SPI interface (possible to messure it by an	IOT-36					10T Sprine 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://llees.slack.com/files-psi/TCZR1HLDT+FGK3ZH075/pins.png Å Å As a developer of folTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **decumentation of the message **ull contain reasoning Å * the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system **message row to recreate this message in CPU is described Acceptance criteria: **message row to reasoning A remassage in CPU is described As a developer of loTester I need to measure the size limit of PRU message system **message row to recreate this message in CPU is described Acceptance criteria: **message to PRU message size limit **message to ease able to write the whole curve to PRU As a developer of loTester I need to send a simple message to SPI interface **hoceptance criteria: **prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU **enabled SPI and GPIOs which are necessary to control DAC in the device tree **enablishy message to SPI interface [possible to messure it by an oscilloscope]	IOT-36					10T Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pin: 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGI3ZH075/pins.png] Å As a developer of forExter I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) *message should be easy to use for PRU (no parsing, no caching in PRU,) *message will support all digital outputs and analog outputs usable on lottester *documentation of the message - will contain reasoning & the basic lies also how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message yetem Acceptance criteria: *messure PRU message size limit *messare if we are able to write the whole curve to PRU *As a developer of forExter I need to send a simple message to SPI interface *Acceptance criteria: *prepare a simple program to work with SPI interface - the program is compliable and possible to load into PRU *anabled SPI and GPOs which are necessary to control DAC in the device tree *send simply message to SPI interface (possible to messure it by an	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://lles.slack.com/files-ps/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of folTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be message.**ull contain reasoning A **the basic idea how to create this message in CPU is described Acceptance criteria: **message PRU message size limit **message representation of the message in CPU is described Acceptance criteria: **prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU **anabled SPI and GPIOs which are necessary to control DAC in the device tree **send simply message to SPI interface (possible to measure it by an oscilloscope) As a user of lo Tester I need the documentation of REST API to be able to use this interface.	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://llies.slack.com/files-psi/TCZR1HLDT+GK3ZH075/pins.png Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a message should be easy to use for PRU (no parsing, no caching in PRU,) **a documentation of the message. **ull contain reasoning A **the basic idea how to create this message in CPU is described Acceptance criteria: **messare PRU message size limit **messare PRU message size limit **neassure FW was a able to write the whole curve to PRU As a developer of to Tester I need to send a simple message to SPI interface **acceptance criteria: **prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU **anabel SPI and GPIOs which are necessary to control DAC in the device tree **send simply message to SPI interface [possible to measure it by an oscilloscope] Acceptance criteria: **REST API nesible to set digital and analog outputs of loTester	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files-psi/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files-psi/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,)	IOT-36					10T Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Comect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files-per/TCZR1HLDT+GK3ZH075/pins.png Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU, message should be easy to use for PRU (no parsing, no caching in PRU, *message should be easy to use for PRU (no parsing, no caching in PRU, *message should be easy to use for PRU (no parsing, no caching in PRU, *message should be easy to use for PRU (no parsing, no caching in PRU, *message should be easy to use for PRU (no parsing, no caching in PRU, *message should be no lollester *decoumentation of the message. *will contain reasoning. *the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system Acceptance criteria: *messaure if we asked beto write the whole curve to PRU As a developer of loTester I need to send a simple message to SPI interface *Acceptance criteria: *prepare a simple program to work with SPI interface - the program is compilable and possible to losal into the NPU *message is simply message to SPI interface (possible to measure it by an oscilloscope) As a user of loTester I need the documentation of REST API to be able to use this interface. *REST API is not Comolèes specific *REST API is not Comolèes specific *BEST API is not Comolèes specific *Documentation of REST APIÄ Hitti:	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU	IOT-118 IOT-97 IOT-91	Sub-task Sub-task Story	To Do Closed In Progress		*Code composer *Commet to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files-psi/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,)	IOT-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 30,0194/198/19c to mode 0x03 [Result https://liles.slack.com/files-pir/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of forTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message will support all digital outputs and analog outputs usable on loTester **documentation of the message - will contain reasoning **the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system **Acceptance criteria: **measure PRU message size limit **measure PRU message size limit **measure PRU message size limit **measure FRU message size limit **measure FRU message size limit **measure of the program to work with SPI interface - the program is compilable and possible to load into PRU **analote SPI and GPIOs which are necessary to control DAC in the device tree of the program of the	107-36					10T Sprine 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files-psi/TCZR1HLDT-FGK3ZH075/pins.png] Å Aa a developer of folTester I need a design of the communication message between PRU and CPU. Acceptance criteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) **amessage will support all digital outputs and analog outputs usable on folTester in the state of the	107-36					10T Sprine 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 30,0194/198/19c to mode 0x03 [Result https://liles.slack.com/files-pir/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of forTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message will support all digital outputs and analog outputs usable on loTester **documentation of the message - will contain reasoning **the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system **Acceptance criteria: **measure PRU message size limit **measure PRU message size limit **measure PRU message size limit **measure FRU message size limit **measure FRU message size limit **measure of the program to work with SPI interface - the program is compilable and possible to load into PRU **analote SPI and GPIOs which are necessary to control DAC in the device tree of the program of the	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU BBB for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Commet to BB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://lles.slack.com/files-ps/TCZR1HLDT-FGK3ZH075/pins.png Å Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be proposed to the stage in the stage in CPU is described no lollester **message should be now to create this message in CPU is described Acceptance criteria: **message PRU message size limit **message representation of the stage in the	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	
whole curve to PRU 888 for All Setup Device Tree Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-97 IOT-93 IOT-96	Sub-task Sub-task Story	To Do Closed In Progress To Do		*Code composer *Comect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://liles.slack.com/files.pri/TCZR1HLDT+GK3ZH075/pins.png] Å As a developer of foTester I need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be easy to use for PRU (no parsing, no caching in PRU, **message should be no located to lead to lead to located located to located to located located located located located loc	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7 IOT Sprint 7	5.0	

					As a user of IoTester I want to be able to set the analog and digital outputs. Acceptance criteria:											
REST API for analog	IOT-83	Story	To Do		* IoTester Rest API provides a call which allows to set digital and analog outputs of IoTester	IOT-36										
output of loTester	10.03	Story	1000		* the RestAPI handler sends the data as a message to RTU HINT:	101 30										
					The handler can prepare the data in a "RTU friendly" form.											
Run ComoNeo					As a user I want to test the measured data on ComoNeo.											
measurement with one curve	IOT-117	Story	To Do		Acceptance criteria: * one curve is set from REST API to the PRU * the curve contains 100 points	IOT-36										
					* the curve is displayed on ComoNeo As a user I want to be able to test an analog output on IoTester to be able to test analog input of ComoNeo.											
Test analog inputs on ComoNeo	IOT-36	Epic	To Do		Acceptance criteria:		ComoNeo Analog Inputs									
on comoneo					* test in robot framework: ** configures loTester to send an analog signal ** checks if the signal was measured by ComoNeo											
					As a user I want to use all analog and digital outputs of loTester to be able to control ComoNeo.											
Enable multiple digital and analog outputs	IOT-86	Story	To Do		Acceptance criteria: * Rest API is extended so that it allows configuration of all digital and	IOT-40										
					analog outputs * RTU executes the configuration according defined timing											
					As a ComoNeo tester I want to be able to take the data for ComoNeo simulator and configure with the IoTester											
					Acceptance criteria: * Robot framework keyword which will load configuration from ComoNeo											
					fpga simulator and configures IoTester via Rest API ComoNeo Simulator input data description:											
					[https://git.kistler.com/comong/comong-											
ComoNeo simulator data conversion	IOT-85	Story	To Do		software/tree/master/Core/lib/Fpga/Simulator] ComoNeo Simulator input data examples:	IOT-40										
					[https://git.kistler.com/comong/comong-software/tree/release- 3.0/Testing/RestApi-Robot/Setups/2molds/ApplicationFiles/Simulator]											
					[https://git.kistler.com/comong/comong-											
					software/tree/master/Testing/RestApi- Robot/Setups/8c1p/ApplicationFiles/Simulator]											
					Ä 8											
Prepare Document	IOT-49	Story	In Progress	Miroslav Sabo	As a hardware engineer, IA want to create document for board design, so that we can use it as a guideline for creating final design of our new	IOT-1				IOT Sprint 3					8.0	
for Board Design		,			board.Å As a user of IoTester I want to be able toÅ run measurement on ComoNeo											
Run ComoNeo					Acceptance criteria: * PRU application sets measurement start digital input of ComoNeo and							IOT Sprint				
measurement	IOT-92	Story	In Progress	Rastislav Kováč	sets one value to the DAC converter * The dac value is possible to set via REST API	IOT-36					IOT Sprint 4	5			21.0	
					* Robot Framework test checks if the cycle started and checks if the value is as expected											
Kistler VPN Access Deployment	IOT-28 IOT-89	Task Epic	Closed To Do	Lukáš Ondriga			Deployment	IOT Sprint 1	IOT Sprint 2							
Start webserver	107.116	54	T. D.		As a user I want to have access to the loTester REST API after boot.	IOT-89										
automaticaly	IOT-116	Story	To Do		Acceptance criteria: * IoTester python webserver is added to Yocto build * REST API is available after IoTester boot.	101-89										
					As a user I want to have the PRU software loaded automaticaly after boot.											
Load PRU exe automaticaly	IOT-115	Story	To Do		Acceptance criteria: * PRU application is added to Yocto build	IOT-89										
					* after the boot of generated image the PRU application is started As a user of IoTester I want my device to be configured automaticaly with											
Configure device					the correct device tree.											
tree for SD card image generation	IOT-114	Story	To Do		Acceptance criteria: * device tree configuration added to Yocto layer * generated image can boot and device tree is configured in correct way	IOT-89										
					(e.g. spi bits have correct mode) As a developer of loTester I need SD card image which contains flusk to be											
Add flusk into SD card image	IOT-113	Story	To Do		able to develop loTester application. Acceptance criteria:	IOT-89										
generation	101115	5.01	1000		* new layer added to yocto configuration * image configuration including flusk added	101 03										
					* generated image can be load to the sd card and the flusk is installed As a developer I need to be able to generate new SD card image.											
SD card image	IOT-81	Story	To Do		Acceptance criteria: * Script for building SD card image from existing yocto configuration for TI	IOT-89										
generation	10.01	Story	1000		Processors SDK is created * Script is available in Git repository	101 03										
Update Retrospertive in	IOT-111	Task	Closed	Stanislav Širka	* It is possible to load generated image to the SD card and run it on BBB								IOT Sprint			
Retrospective in Trello Prepare													6 IOT Sprint			
Presentation for Sprint 6	IOT-110	Task	Closed	Stanislav Širka									6			
Refactoring HW for better compactness	IOT-1	Epic	To Do				IoTester Refactoring									
New Informations in JIRA Tasks	IOT-112	Task	Closed	Stanislav Širka									IOT Sprint 6			
Export JIRA Tasks for Sprint 6 Start Prepare Robot	IOT-109	Task	Closed	Stanislav Širka									IOT Sprint 6			
Framework test for testing cycle values	IOT-108	Task	In Progress	Marián Ján Franko		IOT-36							IOT Sprint 6			
Finalyze Interface between COMONEO	IOT-104	Sub-task	Closed	Tomáš Bujna	https://git.kistler.com/FIIT/lotester/tree/RF/BBB/RESTAPI						IOT Sprint 4	IOT Sprint	IOT Sprint	IOT Sprint		
and BBB Measure PRU	10.2.1.	6111	<i>c</i> .	T (7.5 :	https://git.kistler.com/FIIT/iotester/tree/IOT-107/IOT-								IOT Sprint	IOT Sprint		
message size limit	IOT-107	Sub-task	Closed	Tomáš Bujna	107%20%5BMeasure%20PRU%20message%20size%20limit%5D								6 IOT Sprint	7		
chain concept	IOT-102	Sub-task	In Progress	lgor Labát									6	107		
Compile and Run Simple SPI program	IOT-98	Sub-task	In Progress	Filip Starý									IOT Sprint 6	IOT Sprint 7		
Basic understanding of DAC	IOT-100	Sub-task	Blocked	lgor Labát									IOT Sprint 6			
Understand how the DAC chip is	IOT-101	Sub-task	Blocked	Miroslav Sabo									IOT Sprint			
connected to the board (SPI, GPIOs)		3000											6			

								1		1						
Analyze Analog Output of DAC	ют.99	Story	To Do		As a developer of loTester I need to have a basic understanding of how to communicate with DAC, how to setup DAC to get desire analog value "analyse how the DAC thip is connected to the board SPI, GPIO(3) - which BBB pins are used to control DAC - as an output prepare a simple sketch of PIN description "analyze how to use "basic beautiful properties of the DACsA [https://www.ti.com/lit/ds/lymlinis/dac8734.pdf] "avhat data should be sent via SPI interface to get desired analog value. # how to command DAC to set the analog output "team understands the concept of disiy-chain A [https://www.maximintegrated.com/en/app-notes/indec.mvp/ld/3947]	ЮТ-36							IOT Sprint 6		5.0	
Send constant data to SPI interface	IOT-106	Sub-task	To Do										IOT Sprint 6	IOT Sprint 7		
Create Project Documentation for	IOT-95	Story	Closed	Stanislav Širka								IOT Sprint			8.0	
ZS Digital Input Test	IOT-90	Story	Closed	Stanislav Širka		IOT-2					IOT Sprint 4	IOT Sprint			13.0	
Integration Model Architecture	IOT-80											5				documentation
for Project	101-80	Task	Closed	Stanislav Širka	As a user I want to be able to set digital output from RTU to be able to test					IO1 Sprint 3	IOT Sprint 4					documentation
Program for RTUexe Configuration	ЮТ-9	Story	Closed	Filip Starý	ComoNeo digital input. Acceptance criteria: Running RTU program which sets the digital output of IOTester according configuration from CPU.	IOT-2					IOT Sprint 4				8.0	
Create First Document for	IOT-93	Story	Closed	Stanislav Širka							IOT Sprint 4				8.0	
Project Close Sprint 3	IOT-94	Task	Closed	Stanislav Širka							IOT Sprint 4					
Testing digital inputs on ComoNeo	IOT-2	Epic	To Do				ComoNeo Digital Inputs									
Create Team Poster	IOT-26	Task	Closed													documentation
Decide on Continuous Server	IOT-22	Task	Closed													
Call Program on	IOT-19	Task	Closed			IOT-2										implementation
RTU from CPU Analyze	101-15	Tean	Closed			101-2										implementation
Communication Between RTU and	IOT-18	Task	Closed			IOT-2										analysis
CPU Analyze RTU Choose Simple	IOT-13	Task	Closed			IOT-2										analysis
Program for RTU	IOT-16	Task	Closed			IOT-2										analysis
Server Compatibility	IOT-14	Task	Closed			IOT-2										analysis
					As a user I want to be able to set digital output from RTU to be able to test ComoNeo digital input.											
Load Program to RTU	IOT-17	Task	Closed	lgor Labát	Acceptance criteria: Running RTU program which sets the digital output of IOTester according configuration from CPU.	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3						implementation
Methodics	IOT-44	Story	Closed	Stanislav Širka	Create methodic for: **Meeting Documentation **Tasks managment- done **Methodics - done **Methodics - done **Code versioning - done **Web - done			IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					3.0	
RTU and CPU Communication	IOT-8	Story	Closed	Filip Starý	As a user I need to configure real time simulation to run various simulations. Acceptance criteria:	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					5.0	
Load RTUexe	IOT-7	Story	Closed	lgor Labát	RTU and CPU prototype is running on Beogletone Linux console. As a user I need to do a real time simulation to be able to simulate sensor measurements. Acceptance criteria: Loading of the program to the real time unit will be shown on Linux console.	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					13.0	
Jenkins pipeline for installation image	IOT-88	Story	To Do		Acceptance criteria: * Jenkins pipeline which will be trigerred by the change in a gilt branch and will compose the lofester firmware	IOT-89										
Create Project Specification	IOT-25	Task	Closed	Lukáš Ondriga	·											documentation
Analyze, design, implement REST API	IOT-40	Epic	To Do				REST API									
Manage Kistler resources for PCB design	IOT-53	Task	Closed	Lukáš Ondriga						IOT Sprint 3						
Create Document for Tasks Managment	IOT-52	Task	Closed	Stanislav Širka						IOT Sprint 3						documentation
Presentation for Sprint 3 End	IOT-79	Task	Closed	Stanislav Širka						IOT Sprint 3						
Close Sprint 2 Create Document	IOT-50	Task Task	Closed	Stanislav Širka Stanislav Širka						IOT Sprint 3 IOT Sprint 3						documentation
for Jira Changes REST API Prototype	IOT-10	Story	Closed	Tomáš Bujna	As a user of lOTester I want to have interface to set the Como digital input to be able to configure lOTester. Acceptance criteria: "uncline webserver on beagleboard" "implemented simple post request with value of digital input (0 or 1) "post request execution is logged to the console	IOT-2				IOT Sprint 3					3.0	
Robot Framework LIB	IOT-11	Story	Closed	Marián Ján Franko	As a test developer I want to have a library to use IOTester Acceptance criteria: "synthon module "keywords to set Como digital inputs are implemented HINTIA" inplementation of the keywords are POST requests to the IOTester POST request is implemented in A http://jira.kistler.com/browse/IOT-10	IOT-2				IOT Sprint 3					5.0	
Port loTester specification to Jira	IOT-54	Task	Closed	Lukáš Ondriga						IOT Sprint 3						
Decide on Our Guidelines	IOT-29	Task	To Do													
Create a Test	IOT-12	Story	Closed	Marián Ján Franko	As a user I want to test the ComoNeo digital Input. Acceptance criteria: Test configures IoTester (library for IoTester configuration will be implemented in different user story) Test checks the ComoNeo web application if the digital input was set. Ä	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					13.0	
Analyze Board	IOT-3	Story	Closed	Miroslav Sabo	As a hardware engineer I need to analyse the current board to be able to make the final design. Acceptance criteria: Document the current design of the board.	IOT-1		IOT Sprint 1	IOT Sprint 2						8.0	

Web Server UI-b Story Lucied Rastislav Kovac Acceptance criteria: UI-2 UI-sprint I S.U												 		
Create Perhadic for Methodic of Methodic														
Create Mode State Crea	Close Sprint 1	IOT-48	Task	Closed	Stanislav Širka					IOT Sprint 2				
Month Mont						Export tasks from Jira.								
Methods First Fi		IOT-30	Story	Closed	Tomáš Ruina				IOT Sprint 1			1	8.0	
Chooke Web Server Catchrology Closed Clo													0.0	
Technology for Linux Fine Closed Fastisial Vow Closed Fasti	Print User Stories	IOT-47	Task	Closed	Stanislav Širka				IOT Sprint 1					
Part		IOT-15	Task	Closed	Rastislav Kováč		IOT-2		IOT Sprint 1					
RST AP for BeagleRone Black real time unit configurations. No.														
Methodis District		IOT-6	Story	Closed	Rastislav Kováč	REST API for BeagleBone Black real time unit configurations. Acceptance criteria:			IOT Sprint 1				5.0	
December	Create Methodic for													
	Methodics	IOT-45	Task	Closed	Stanislav Širka				IOT Sprint 1			1		documentation
Start Sprint Start Sprint Start Sprint Start Sprint Start Sprint														
Add Tasks to Jira 17-20 Task Closed Stanislav Sira Closed Closed Closed Stanislav Sira Closed Closed Stanislav Sira Closed Closed Stanislav Sira Closed Closed Stanislav Sira Closed Stanislav Sira Closed Stanislav Sira Cl		IOT-46	Task	Closed	Stanislav Širka				IOT Sprint 1					
Write TIT Close Table Close Sanisla Siria Close Close Close Close Siria Siria Close Close Siria Siria Siria Close Close Siria Siria Siria Close Close Siria Siria Siria Siria Close Close Siria Siri	Add Tasks to Jira	IOT-20	Task	Closed	Stanislav Širka	* Create Sprint - done * Add tasks to Sprint - done			IOT Sprint 1					
Requirements 101-32 Task Closed Stanislav Sirita	Share Google Drive	IOT-24	Task	Closed										
Create Frank Off 23 Task Closed		IOT-32	Task	Closed	Stanislav Širka									
Update Freillo OF-13 Task Closed Sanislav Sirka Closed Sanislav Sirka Closed	Study SCRUM	IOT-33	Task	Closed	Stanislav Širka									
Deciration Dec								The state of the s						
Document 107-27 Task Closed General and educamentation Document Document Closed Statistics Closed Closed </td <td></td> <td>IOT-31</td> <td>Task</td> <td>Closed</td> <td>Stanislav Širka</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>		IOT-31	Task	Closed	Stanislav Širka									
Study Story Points 107-35 Task Closed Stanislav Širla Study Study Story Points 107-35 Task Closed Stanislav Širla Study		IOT-27	Task	Closed										documentation
	Study Poker Cards	IOT-34	Task	Closed	Stanislav Širka									
Create Team GIT IOT-21 Task Closed Close	Study Story Points	IOT-35	Task	Closed	Stanislav Širka									
	Create Team GIT	IOT-21	Task	Closed										